

JOURNAL

JOURNAL OF MEASUREMENTS, ELECTRONICS,
COMMUNICATIONS & SYSTEMS (JMECS)
2015

Bandung, 15 December 2015

COMMITTEE

Editor in Chief: Dr. Erna Sri Sugesti

Editors:

Dr. Bambang Setia Nugroho	(Editor for Communication System)
Sigit Yuwono, PhD	(Editor for Electronics System)
Erwin Susanto, PhD	(Editor for Control System)
Dr. Indra Fathona	(Editor for Measurement System)
Angga Rusdinar, PhD	(Editor for Autonomous & Robotics)
Dr. Bambang Hidayat	(Editor for Signal Processing)

Associate Editors:

Prof. Harijono A. Tjokronegoro	ITB
Dr. Heroe Wijanto	Telkom University
Dr. Rendy Munadi	Telkom University
Dr. Bambang Hidayat	Telkom University
Ali Muayyadi, PhD	Telkom University
Dr. Rina Pudjiastuti	Telkom University
Dr. Erna Sri Sugesti	Telkom University
Dr. Basuki Rahmat	Telkom University
Augie Widjyotriatmo, PhD	ITB
Ade Haryanto, PhD	ITB
Dr. Ing. Fiky Yosep Suratman	Telkom University
Dr. Arfianto Fahmi	Telkom University
Dr. Ismudiati Puri Handayani	Telkom University
Dr. Memoria Rosi	Telkom University
Dr. Abrar	Telkom University
Dr. Ery Junaedi	Telkom University
Dr. Basuki Rahmat	Telkom University
Dr. Ida Wahidah	Telkom University
Dudi Adi Firmansyah, PhD	Telkom University
Dr. Levy Olivia Nur	Telkom University

GREETING

I am delighted to be the first-term Editor-in-Chief of JMECS as of March 1, 2015.

My duty is to accelerate JMECS to be a prestigious journal in the international Physics, Electronics, and Communications engineering community.

In order to reach the targets, we will make the review cycle to be done as fast as possible and keep the journal in high quality.

I would like to express my gratitude to editors, Associate Editors, and Reviewers of JMECS for their service and dedication to the editorial works.



Dr. Erna Sri Sugesti
Editor-in-Chief

CONTENTS

Cover	i
Committee	ii
Greeting	iii
Contents	iv

COMMUNICATION SYSTEM

1	Analysis and Simulation of Wavelet Carrier Modulation with Clipping Techniques in Mobile WiMAX <i>Muhamad Mardanu Kusuma, Rina Pudji Astuti, Dharu Arseno</i>	A-1
2	Improvement in the LDPC Error Correction Process Based on FPGA Implementation <i>Tarigan Aditia, Rita Purnamasari, Efa Maydhona Saputra</i>	A-10

SIGNAL PROCESSING

1	Wrapper Feature Subset Selection for Feature Extraction of Bonang Barung Single Tone Conversion Into Numeric Notation <i>Inung Wijayanto, Nurina Listya Hakim, Achmad Rizal</i>	B-1
2	Audio Steganography using Modified Enhanced Least Significant Bit in 802.11n <i>H. Carolus Ferdy Setiaji, T. Suhartono, H.Bambang</i>	B-7

MEASUREMENT SYSTEM

1	Simulation of Temperature Distribution in Horizontal Fin Heat Sink CPU Processor Using Comsol Multiphysics and Proportional Control <i>T.D. Sugiarto, R.F. Iskandar, I.P. Handayani</i>	C-1
---	--	-----

AUTONOMOUS & ROBOTICS

1	Design and Implementation System Automatic Guided Vehicle (AGV) Using RFID for Position Information <i>Ibnu Waldy, Angga Rusdinar, Estananto</i>	D-1
---	---	-----